

Electronic Acknowledgement Receipt

EFS ID:	9387222
Application Number:	10681577
International Application Number:	
Confirmation Number:	6797
Title of Invention:	USE OF ANALOG-VALUED FLOATING-GATE TRANSISTORS TO MATCH THE ELECTRICAL CHARACTERISTICS OF INTERLEAVED AND PIPELINED CIRCUITS
First Named Inventor/Applicant Name:	Christopher J. Diorio
Customer Number:	46188
Filer:	Dohyun Ahn
Filer Authorized By:	
Attorney Docket Number:	IMPJ-0004 033327-000036
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Assignee showing of ownership per 37 CFR 3.73(b).	17843_373b.pdf	37487 81837ea8a89c49789a2d365035a734d82d0f5fcd	no	2

Warnings:

Information:

2	Power of Attorney	17843_POA.pdf	124495	no	2
			0d561ef88cd4706d99555811b43ec1c5b9b686bd		
Warnings:					
Information:					
3	Change of Address	17843_Fee_Address_Form_Additional_Sheet.pdf	42436	no	1
			4f3a891bbbc8e990206bee1a0cadaaf37153a7f7		
Warnings:					
Information:					
4	Change of Address	17843_Fee_Address_Form.pdf	312629	no	2
			0676473d2c58a39003fec4847c5ef6548cff1f55		
Warnings:					
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Total Files Size (in bytes):			517047		
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